

ABSTRACT OF THE DISCLOSURE

An intelligent transmitter receiver system is constructed to include a CPU, a data-entry keys input circuit, data-entry keys function switch, a DC battery circuit, a DC voltage rectifier circuit,

5 a memory, an indicator lamp circuit, a digital-to-analog converter, a voltage-controlled type high frequency transmitting circuit, a bandwidth extension switching circuit, a mixer circuit, a signal amplifier, a full-channel signal receiving circuit, and a power supply circuit. The bandwidth extension switching circuit extends

10 the working range of the transmitter receiver system. Frequency auto-correction is operated after reading of external code signal so that the efficiency of the system is improved.